

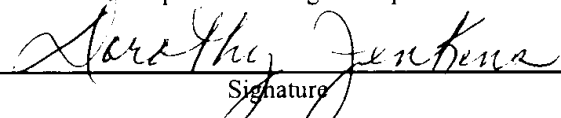
## REMARKS/ARGUMENT

The application is being amended to reduce the Government fees.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as Express Mail # EL157415864US in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on October 5, 2001

Dorothy Jenkins

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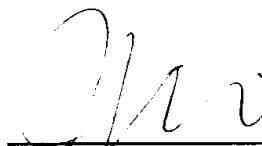


Signature

October 5, 2001

Date of Signature

Respectfully submitted,



James A. Finder

Registration No.: 30,173

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700

JAF:gme

## APPENDIX B

VERSION WITH MARKINGS TO SHOW CHANGES MADE  
37 C.F.R. § 1.121(b)(iii) AND (c)(ii)

### CLAIMS (with indication of amended or new):

5. (Amended) An oscillator according to one of Claims 1 and 3 [1 to 4], further comprising a bias line and a bias resistor both for applying a bias voltage to the active device which are formed on the dielectric substrate.

6. (Amended) An oscillator according to one of Claims 1 and 3 [1 to 4], wherein the microstrip-line resonator and the coupling line are formed at the same time.